

DOCKET NO.: 4001-1222 APPLN NO.: 10/588,860 REPLY TO OFFICE ACTION OF: SEPTEMBER 7, 2007 REPLACEMENT SHEET

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FIG 1 (Prior Art)

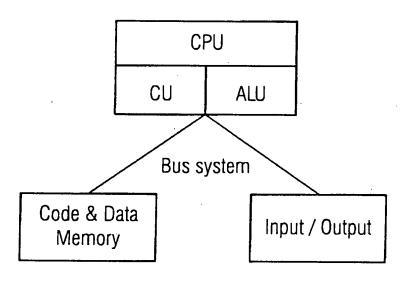
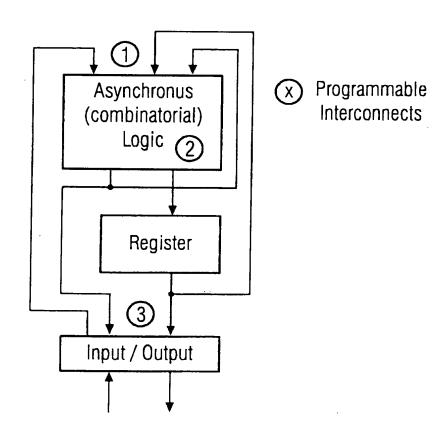


FIG 2 (Prior Art)



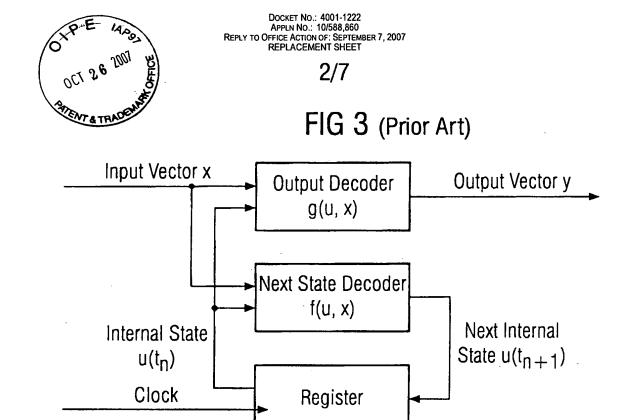
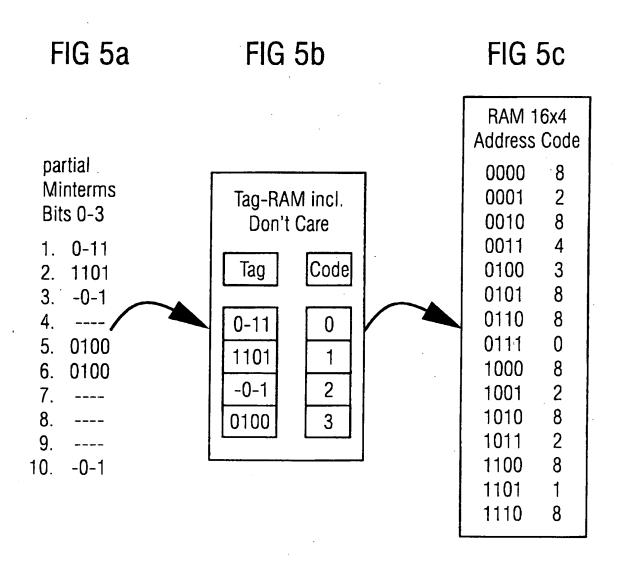


FIG 4 0001 0000 00 1101 Stage 1: ternary CAM 0100 00--11 010100 10100 Stage 2: 1000---0000 ternary RAM --00------01 1100 10011 12



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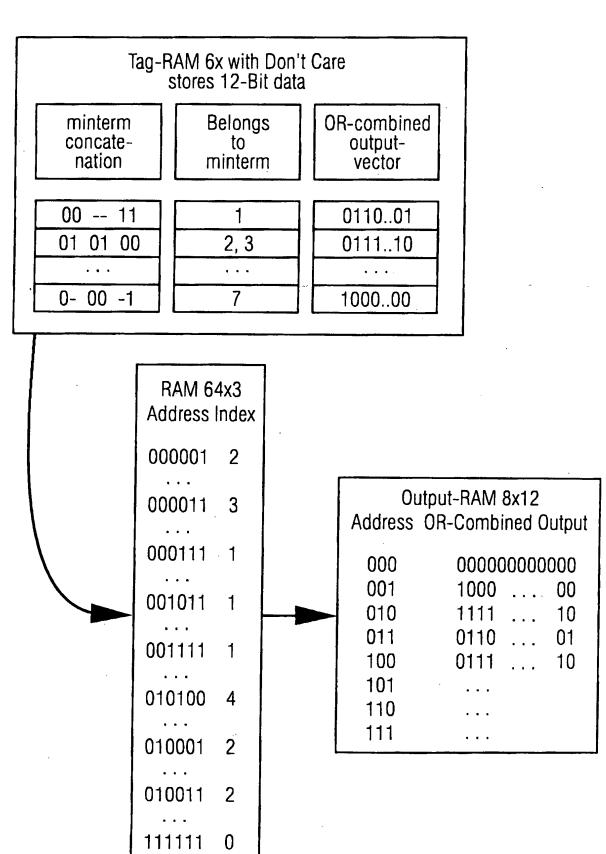


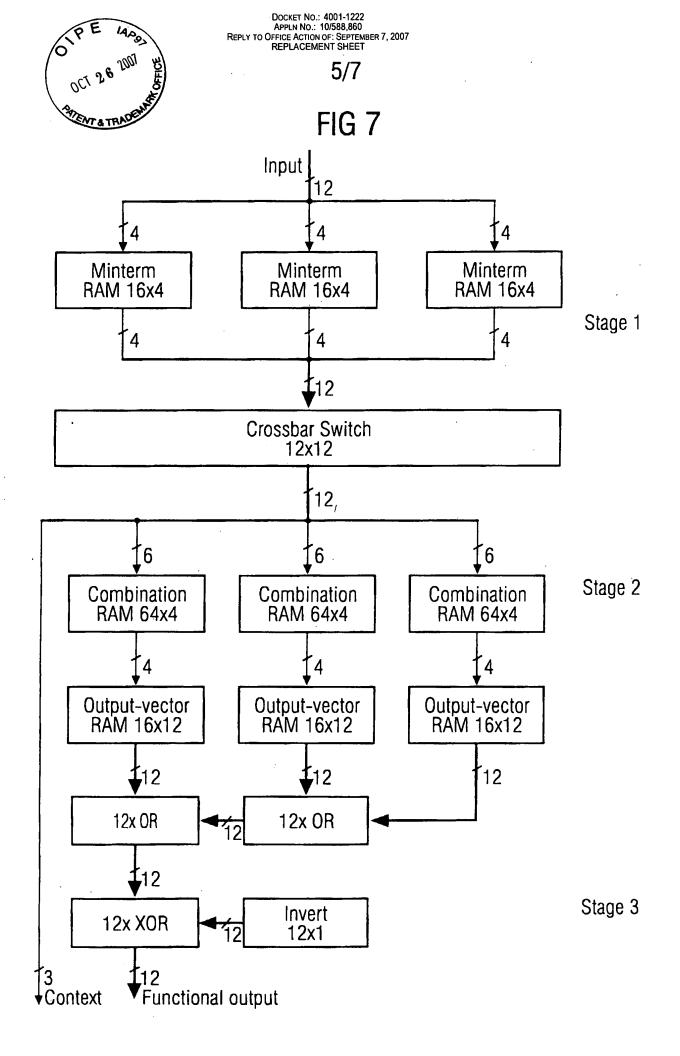


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FIG 6





DOCKET NO.: 4001-1222 APPLN NO.: 10/588,860 REPLY TO OFFICE ACTION OF: SEPTEMBER 7, 2007 REPLACEMENT SHEET 6/7 FIG 8 8 8 8 8 RAM 256*8 RAM 256*8 RAM 256*8 RAM 256*8 RAM 256*{ RAM 256*8 RAM 256*8 8 8 8 8 8 8 64 8 8 64 Crossbar 64*1 64*D-Flipflop 64*=1Crossbar Switch Switch 64* **RAM RAM** 16 RAM RAM 64K*8 256*64 256*64 64K*8 64* 64* 16, 18, **RAM RAM RAM RAM** 64K*8 256*64 256*64 64K*8 64* 64* 16, **RAM RAM RAM RAM** 16, 64K*8 256*64 256*64 64K*8 64 64*<u>></u>1 RAM **RAM** 8 16 **RAM** RAM 16, 64K*8 256*64 256*64 64K*8

FIG 9